

# WEST Search History

DATE: Tuesday, September 16, 2003

<u>Set</u> <u>Name</u> side by side	<u>Query</u>	<u>Hit</u> <u>Count</u>	<u>Set</u> <u>Name</u> result set
	<i>DB=USPT,PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>		
L22	((('6032274')[ABPN1,NRPN,PN,TBAN,WKU]) AnD ((@pd > 20030324)!))	0	L22
L21	(L20 and clock edge with (compress\$3 or compact\$3) same (ic or integrat\$3 near2 circuit\$3) or semi\$1conduct\$7 or memor\$7 or stor\$4 or register\$4)) AnD ((@pd > 20030324)!))	0	L21
L20	(L19 and clock edge with (compress\$3 or compact\$3)) AnD ((@pd > 20030324)!))	3	L20
L19	(clock edge) AnD ((@pd > 20030324)!))	488	L19
L18	(L15 and L16) AnD ((@pd > 20030324)!))	0	L18
L17	(L15 and (compress\$4 or compact\$4) same (pull\$1up or pull\$1down)same latch\$4 same (ic or (integrat\$3 near2 circuit\$3) or semi\$1conduct\$7 or memor\$7) and (clock or timing)same (test\$3 or debug\$4 or verif\$7 or diagno\$5 or probing or probe or exerci\$4)) AnD ((@pd > 20030324)!))	0	L17
L16	(L14 and (compress\$4 or compact\$4) same (pull\$1up or pull\$1down)same latch\$4 same (ic or (integrat\$3 near2 circuit\$3) or semi\$1conduct\$7 or memor\$7) and (clock or timing)same (test\$3 or debug\$4 or verif\$7 or diagno\$5 or probing or probe or exerci\$4)) AnD ((@pd > 20030324)!))	0	L16
L15	(L14 and (compress\$4 or compact\$4) and (pull\$1up or pull\$1down)same latch\$4 same (ic or (integrat\$3 near2 circuit\$3) or semi\$1conduct\$7 or memor\$7) and (clock or timing)same (test\$3 or debug\$4 or verif\$7 or diagno\$5 or probing or probe or exerci\$4)) AnD ((@pd > 20030324)!))	4	L15
L14	(L13 and (compress\$4 or compact\$4) and (pull\$1up or pull\$1down)same latch\$4 same (ic or (integrat\$3 near2 circuit\$3) or semi\$1conduct\$7 or memor\$7) and (clock or timing)) AnD ((@pd > 20030324)!))	11	L14
L13	((compress\$4 or compact\$4) and (pull\$1up or pull\$1down)same latch\$4) AnD ((@pd > 20030324)!))	52	L13
L12	(L1 and (pull\$1up or pull\$1down)same latch\$4) AnD ((@pd > 20030324)!))	0	L12
L11	(L7 and (pull\$1up or pull\$1down)same latch\$4) AnD ((@pd > 20030324)!))	0	L11
L10	((pull\$1up or pull\$1down)same latch\$4) AnD ((@pd > 20030324)!))	187	L10
L9	((('6163491')[ABPN1,NRPN,PN,TBAN,WKU]) AnD ((@pd > 20030324)!))	0	L9

L8	(L7 and double data rate same (compress\$4 or compact\$4) same (test\$3 or debug\$4 or verif\$7 or diagno\$5 or probing or probe or exerci\$4) same (ic or (integrat\$3 near2 circuit\$3) or semi\$1conduct\$7 or memor\$7) and (clock or timing)) AnD ((@pd > 20030324)!) )	0	L8
L7	(L6 and double data rate and (compress\$4 or compact\$4) same (test\$3 or debug\$4 or verif\$7 or diagno\$5 or probing or probe or exerci\$4) same (ic or (integrat\$3 near2 circuit\$3) or semi\$1conduct\$7 or memor\$7) and (clock or timing)) AnD ((@pd > 20030324)!) )	5	L7
L6	(double data rate and (compress\$4 or compact\$4) same (test\$3 or debug\$4 or verif\$7 or diagno\$5 or probing or probe or exerci\$4) same (ic or (integrat\$3 near2 circuit\$3) or semi\$1conduct\$7 or memor\$7)) AnD ((@pd > 20030324)!) )	9	L6
L5	(L1 and (double near (data or rate) or ddr )same (compress\$4 or compact\$4) same (test\$3 or debug\$4 or verif\$7 or diagno\$5 or probing or probe or exerci\$4) and (ic or (integrat\$3 near2 circuit\$3) or semi\$1conduct\$7 or memor\$7)) AnD ((@pd > 20030324)!) )	0	L5
L4	((('6324602')[ABPN1,NRPN,PN,TBAN,WKU]) AnD ((@pd > 20030324)!) )	0	L4
L3	((('6317372')[ABPN1,NRPN,PN,TBAN,WKU]) AnD ((@pd > 20030324)!) )	0	L3
L2	(L1 and (double near (data or rate) or ddr )same (compress\$4 or compact\$4) same (test\$3 or debug\$4 or verif\$7 or diagno\$5 or probing or probe or exerci\$4) same (ic or (integrat\$3 near2 circuit\$3) or semi\$1conduct\$7 or memor\$7)) AnD ((@pd > 20030324)!) )	0	L2
L1	((double near (data or rate) or ddr )same (compress\$4 or compact\$4)) AnD ((@pd > 20030324)!) )	20	L1

END OF SEARCH HISTORY